

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (currently amended). An apparatus for determining tampering in an electricity meter arrangement comprising:

a service disconnect switch operable to controllably disconnect electrical power lines from a load, the load including at least first and second feeder lines;

a voltage sense circuit coupled to sense voltage on the first and second feeder lines, the voltage sense circuit operable to generate a single voltage detection signal based on a first voltage on the first feeder line and a second voltage on the second feeder line, the voltage detection signal having a characteristic representative of whether line voltage from the electrical power lines is present on the first and second feeder lines, ~~the voltage sense circuit further including an isolation mechanism;~~ and

a processing circuit operably connected to the voltage sense circuit to receive the voltage detection signal, the processing circuit operable to selectively generate a tamper flag based on whether the characteristic of the voltage detection signal indicates the presence of voltage on the first and second feeder lines when the service disconnect switch has disconnected the electrical power lines from the first and second feeder lines; ~~and wherein~~

~~—— said isolation mechanism is configured to isolate the processing circuit from the first and second feeder lines.~~

Claim 2 (original). The apparatus of claim 1 wherein the voltage sense circuit is further operable to generate a voltage detection signal having a first magnitude when line voltage is

present on the first and second feeder lines and having a second magnitude when line voltage is not present on the first and second feeder lines.

Claim 3 (original). The apparatus of claim 2 wherein the first magnitude and second magnitude are discrete digital signal levels.

Claim 4 (original). The apparatus of claim 1 wherein the voltage sense circuit includes a voltage divider operable to scale a voltage difference between the first feeder line and the second feeder line.

Claim 5 (currently amended). The apparatus of claim 4 wherein:

the voltage sense circuit further includes an output switch;

the voltage divider includes an output coupled to an output switch, the output switch biased on when the voltage divider provides a voltage exceeding a predetermined level, the output switch operably coupled to provide the voltage detection signal to the processing device, the voltage detection signal varying based on whether the output switch is biased on.

Claim 6 (canceled).

Claim 7 (original). The apparatus of claim 5 wherein the processing circuit is further operable to obtain digital values from a meter measurement circuit and generate metering information therefrom.

Claims 8 and 9 (canceled).

Claim 10 (currently amended). The apparatus of claim 1 wherein the voltage sense circuit ~~isolation mechanism~~ includes an optical isolation circuit.

Claim 11 (previously presented). An apparatus for determining tampering in an electricity meter arrangement comprising:

- a housing containing a metering circuit;

- a service disconnect switch disposed within the housing, the service disconnect switch operable to controllably disconnect electrical power lines from a load, the load including at least first and second feeder lines;

- a voltage sense circuit coupled to sense voltage on at least one feeder line, the voltage sense circuit including an isolation mechanism interposed between the at least one feeder line and an output to provide electrical isolation therebetween, the voltage sense circuit operable to generate a voltage detection signal having a characteristic representative of whether line voltage from the electrical power lines is present on the at least one feeder line, the voltage sense circuit operable to provide the voltage detection signal to the output; and

- a processing circuit operably connected to the output to receive the voltage detection signal, the processing circuit operable to selectively generate a tamper flag based on whether the characteristic of the voltage detection signal indicates the presence of voltage on the at least one feeder line when the service disconnect switch has disconnected the electrical power lines from the first and second feeder lines.

Claim 12 (original). The apparatus of claim 11 wherein the voltage sense circuit is further operable to generate a voltage detection signal having a first magnitude when line voltage is present on the at least one feeder line and having a second magnitude when line voltage is not present on at least one feeder line.

Claim 13 (original). The apparatus of claim 12 wherein the first magnitude and second magnitude are discrete digital signal levels.

Claim 14 (previously presented). The apparatus of claim 11 wherein the voltage sense circuit includes an output switch, the output switch biased on when line voltage is present on the at least one feeder line, the output switch operably coupled to provide the voltage detection signal to the output, the voltage detection signal varying based on whether the output switch is biased on.

Claim 15 (original). The apparatus of claim 14 wherein the output switch is an optical receiver, the optical receiver including a portion of the isolation mechanism.

Claim 16 (original). The apparatus of claim 11 wherein the isolation mechanism includes an optical isolation circuit.

Claim 17 (previously presented). A method comprising:

a) disconnecting, using a service disconnect switch, at least one feeder line of a load from at least one electrical power line, the service disconnect switch disposed within an electricity meter housing;

b) employing a voltage sense circuit that is operably connected to the at least one feeder line to generate a voltage detection signal having a characteristic representative of whether line voltage from the electrical power lines is present on the at least one feeder line;

c) providing the voltage detection signal to an output that is electrically isolated from the at least one feeder line;

d) employing a processing circuit to receive the voltage detection signal and generate a tamper flag if the characteristic of the voltage detection signal indicates the presence of line voltage on the at least one feeder line.

Claim 18 (original). The method of claim 17 wherein step b) further includes employing the voltage sense circuit to generate the voltage detection signal such that the voltage detection signal has a first magnitude when line voltage is present on the at least one feeder lines and the voltage detection signal has a second magnitude when line voltage is absent from the at least one feeder line.

Claim 19 (original). The method of claim 18 wherein step b) further includes employing the voltage sense circuit to generate the voltage detection signal such that the first magnitude and the second magnitude are discrete digital voltage levels.

Claim 20 (canceled)

Claim 21 (previously presented). The method of claim 17 wherein step c) further comprises providing the voltage detection signal to the output using optical isolation.

Claim 22 (new). The apparatus of claim 1, wherein the processing circuit includes a single input operably coupled to receive the voltage detection signal.

Claim 23 (new). The method of claim 17, wherein step c) further comprises employing the voltage sense circuit to generate the voltage detection signal by generating the voltage detection signal to have a characteristic that is representative of a voltage differential between two feeder lines.